

using the analog circuitry to capture information data from an incoming stream for a first time interval while the digital signal processing circuitry is in a reduced-activity mode; and

in a mode other than the reduced-activity mode and during a second shorter time interval, clocking the digital signal processing circuitry to permit digital signal processing of the captured information data.

B1
cont

2. (Amended) A method for reducing noise passed from the digital signal processing circuitry, according to claim 1, wherein the mode other than the reduced-activity mode occurs during a known guard time for the data being communicated to the communication arrangement.

9. (Amended) A method for reducing noise passed from the digital signal processing circuitry, according to claim 1, further including the steps of: providing a memory circuit coupled for access by at least a portion of the analog circuitry and by at least a portion of the digital signal processing circuitry; using said at least a portion of the analog circuitry to read data out of the memory circuit and using said at least a portion of the digital signal processing circuitry to write data into the memory circuit.

B2

10. (Amended) A method for reducing noise passed from the digital signal processing circuitry, according to claim 1, further including the steps of: providing a memory circuit coupled for access by at least a portion of the analog circuitry and by at least a portion of the digital signal processing circuitry; using said at least a portion of the analog circuitry to write data into the memory circuit and using said at least a portion of the digital signal processing circuitry to read data out of the memory circuit.

B3
cont

18. (Amended) A communication arrangement susceptible to processing corrupted data due to noise coupled thereto via high-speed data processing, comprising:
a chip including both digital signal processing circuitry and analog circuitry, the digital signal processing circuitry having a reduced activity mode and a high-speed data